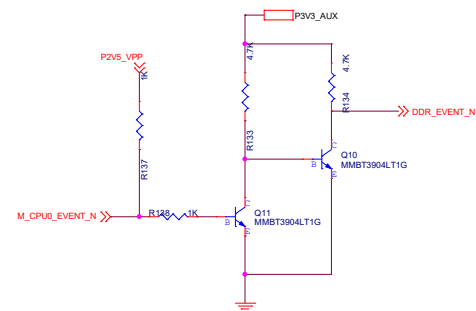
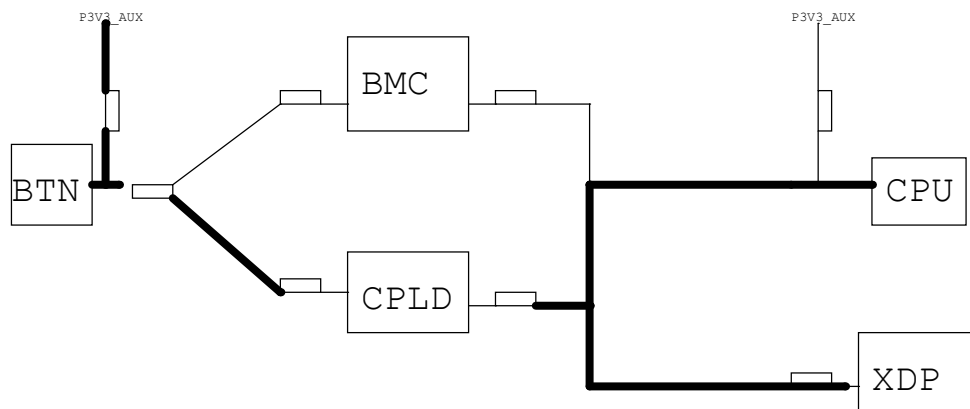
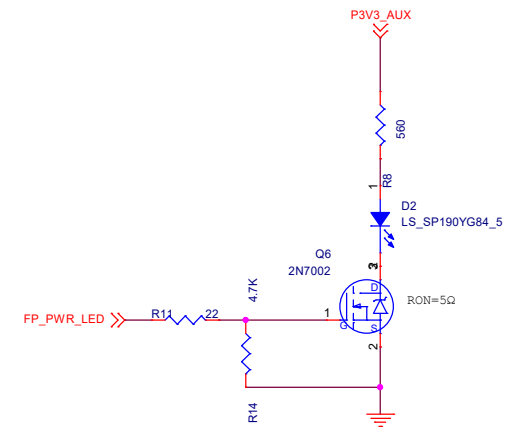
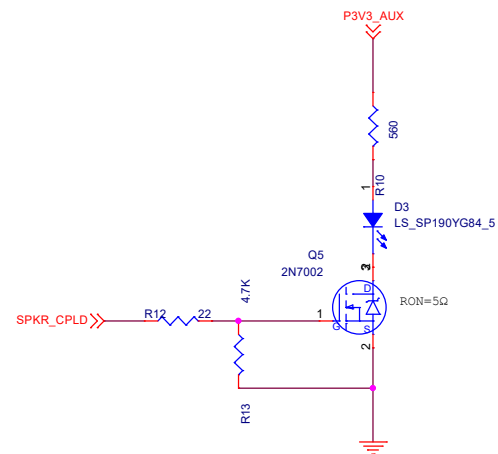
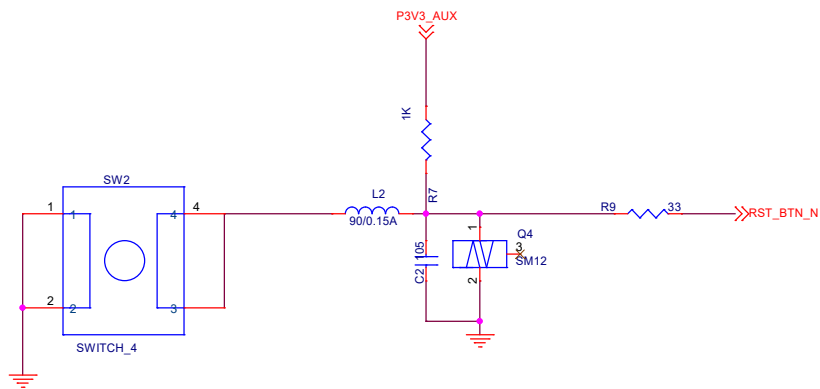
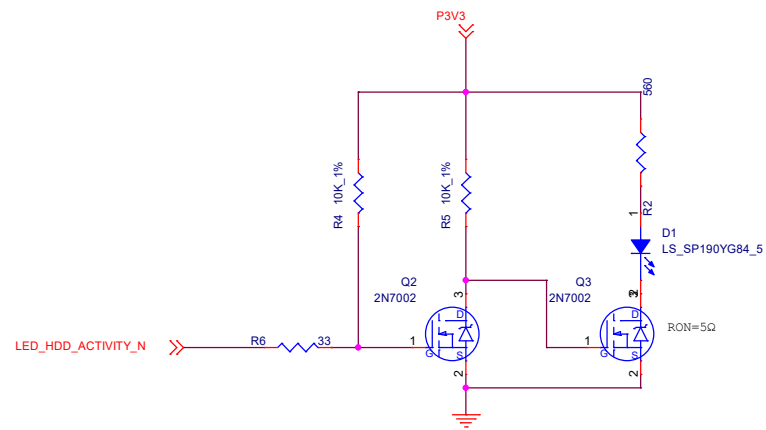
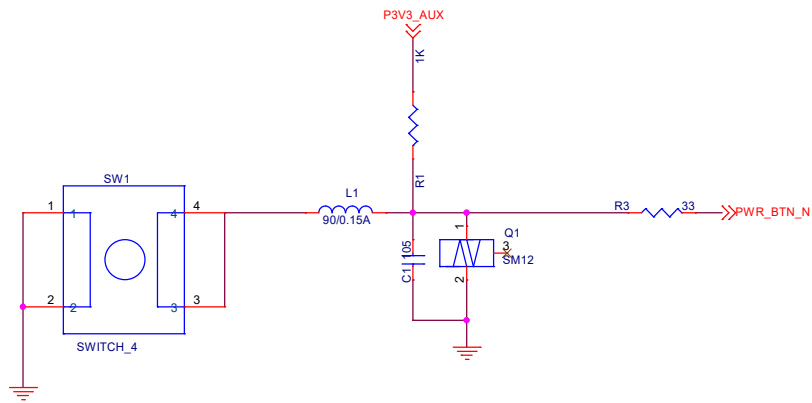
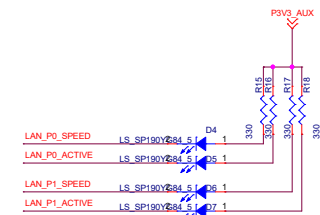


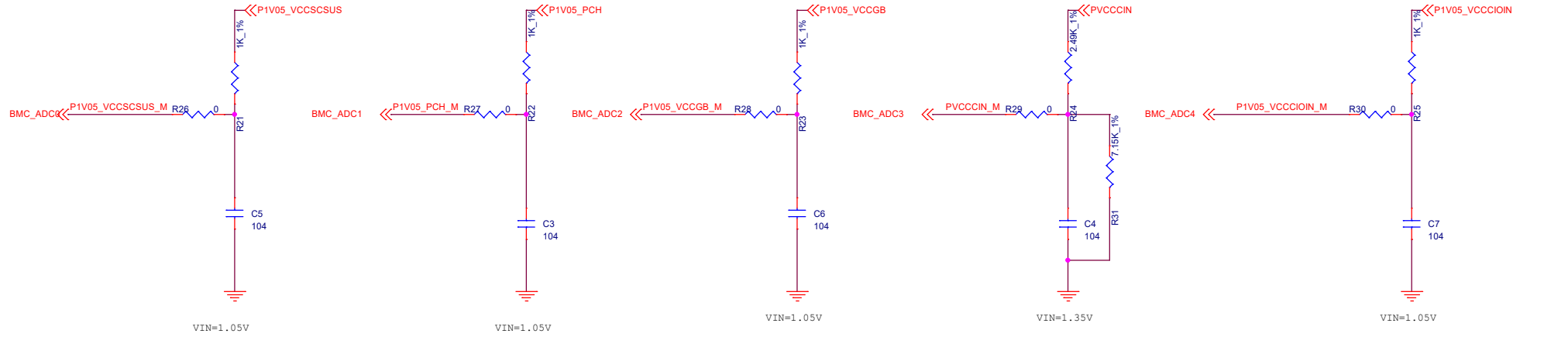




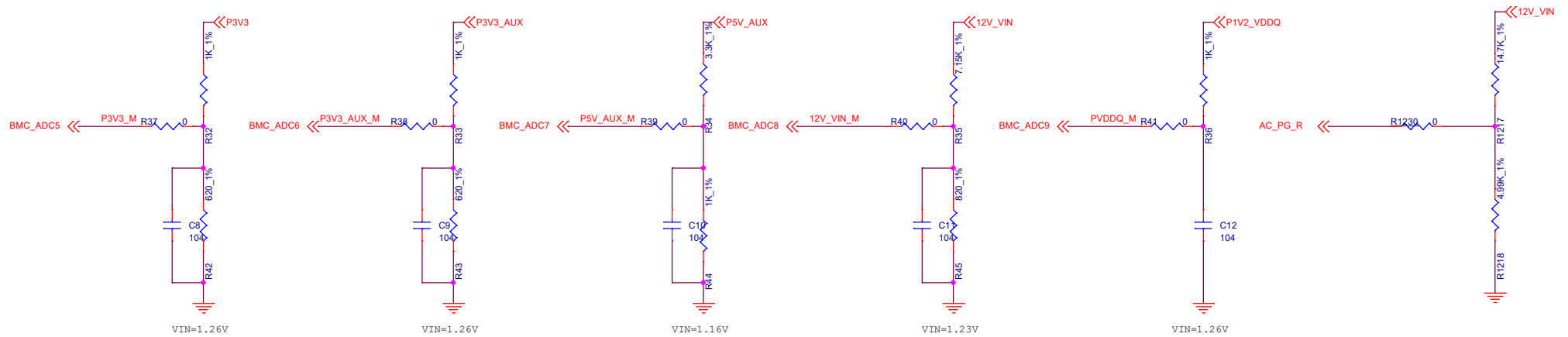


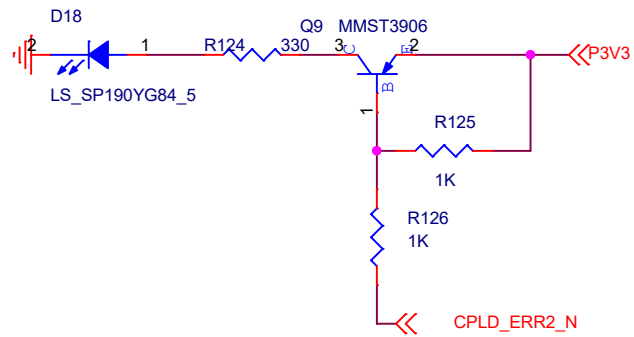
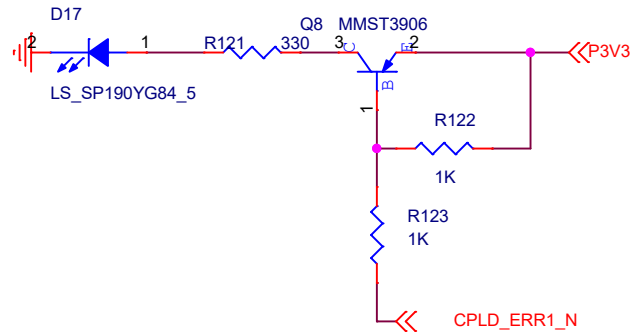
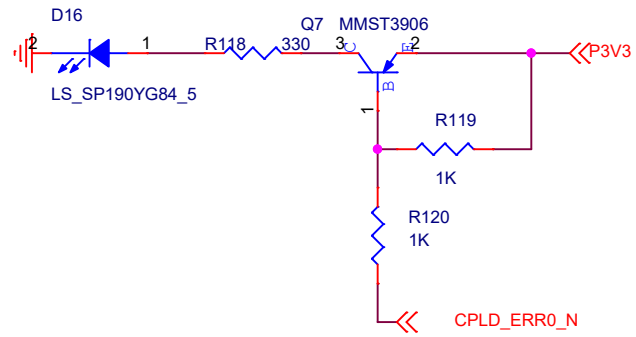


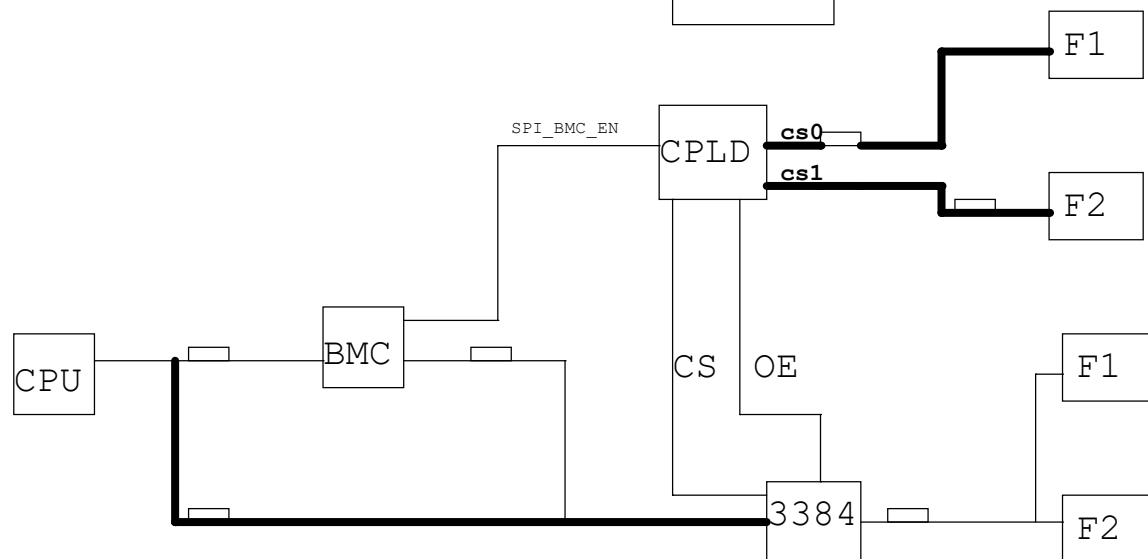
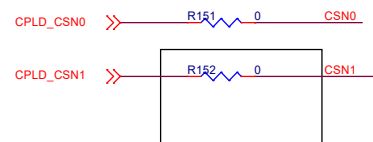
BMC_ADC[0:9] >> _____

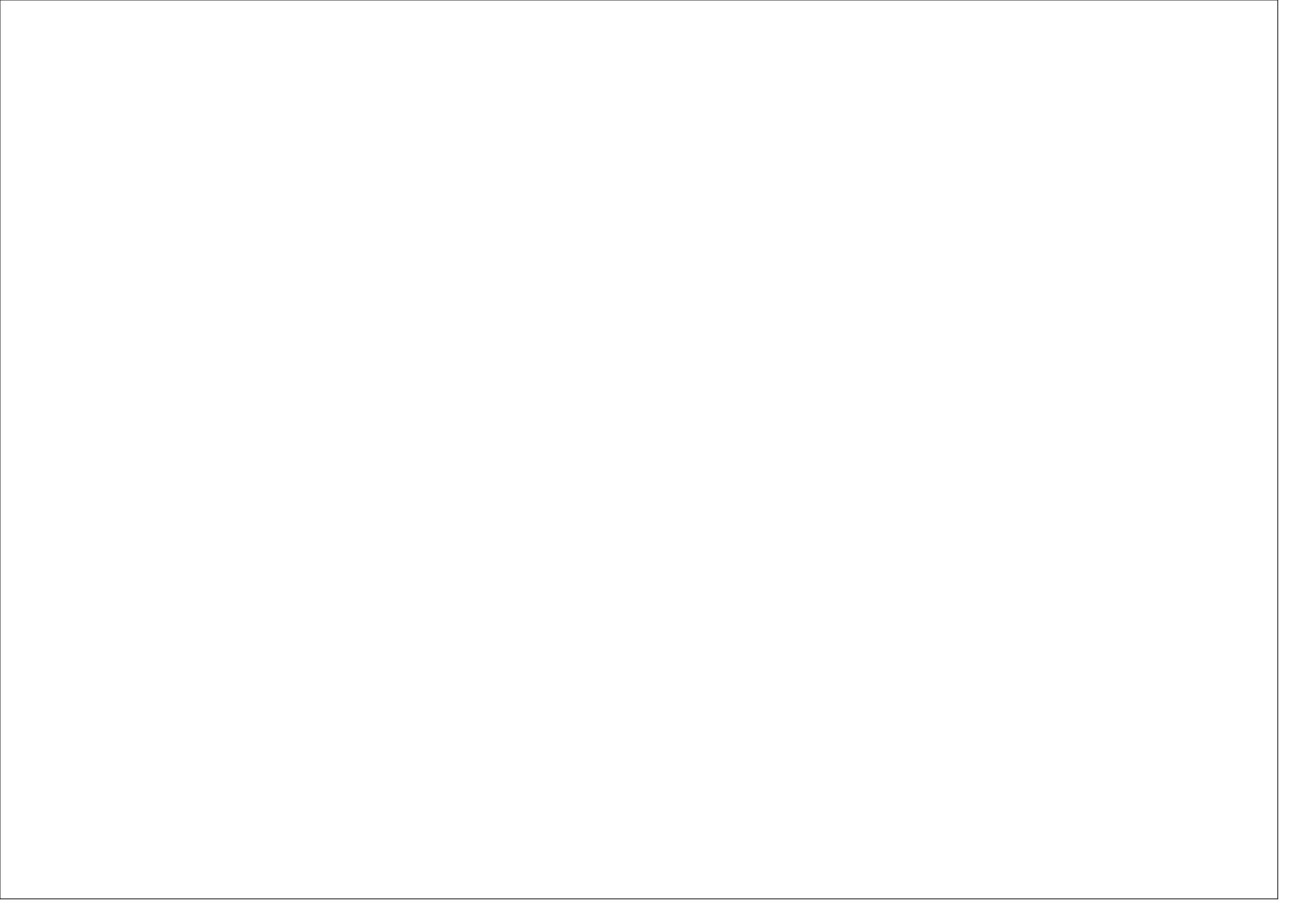


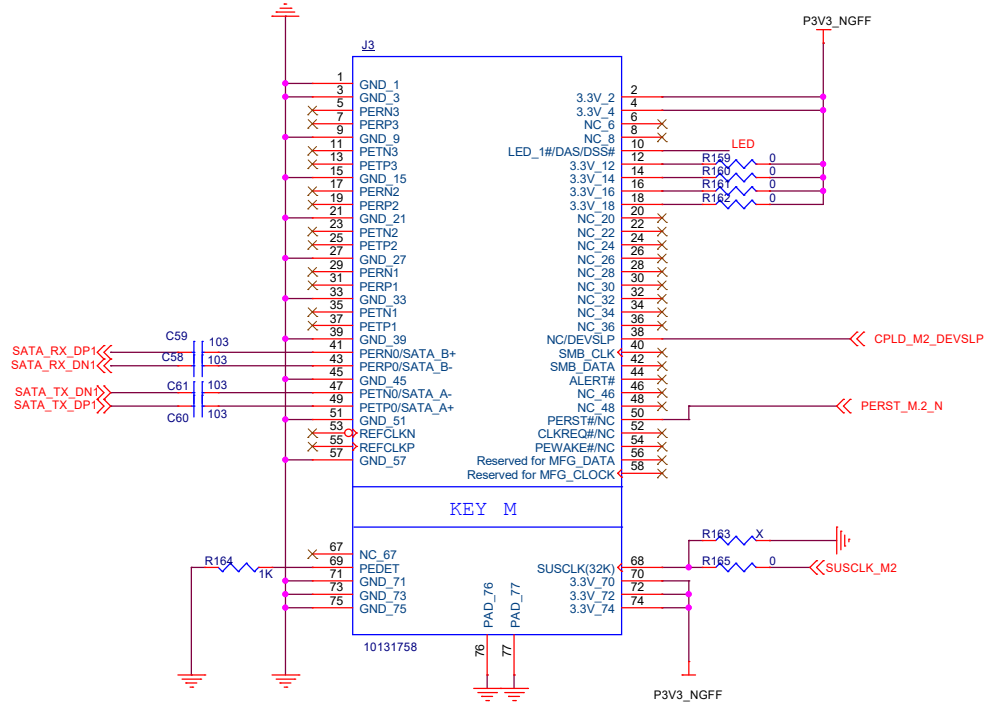
ADC input voltage range : 0 ~ 1.8V 1 . 3 5 V

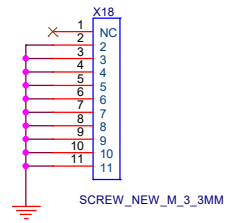
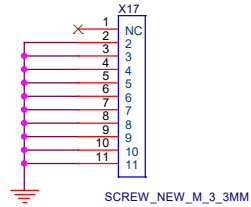
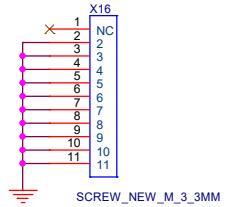
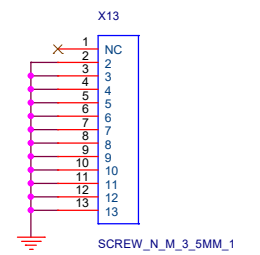
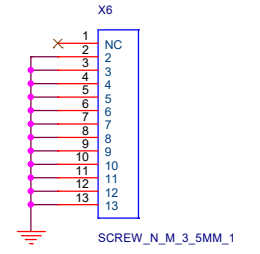
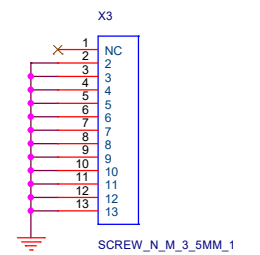
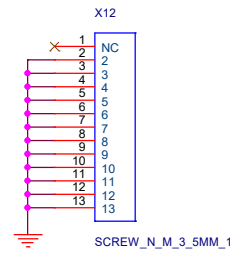
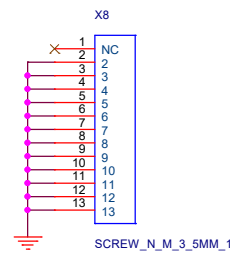
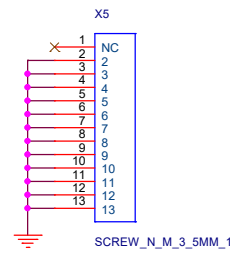
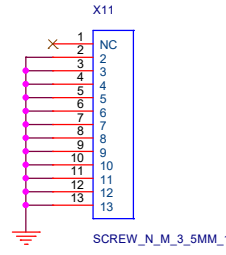
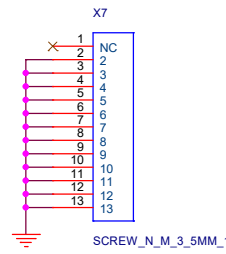
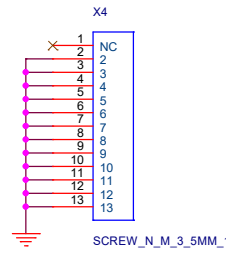
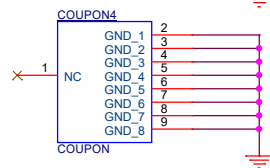
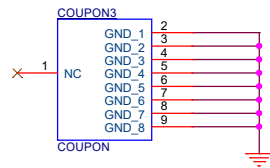
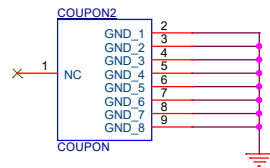
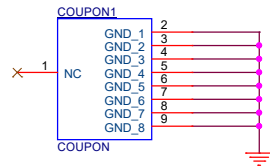
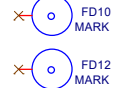
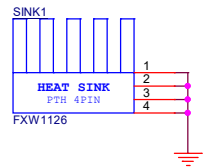
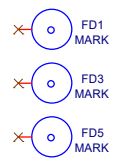


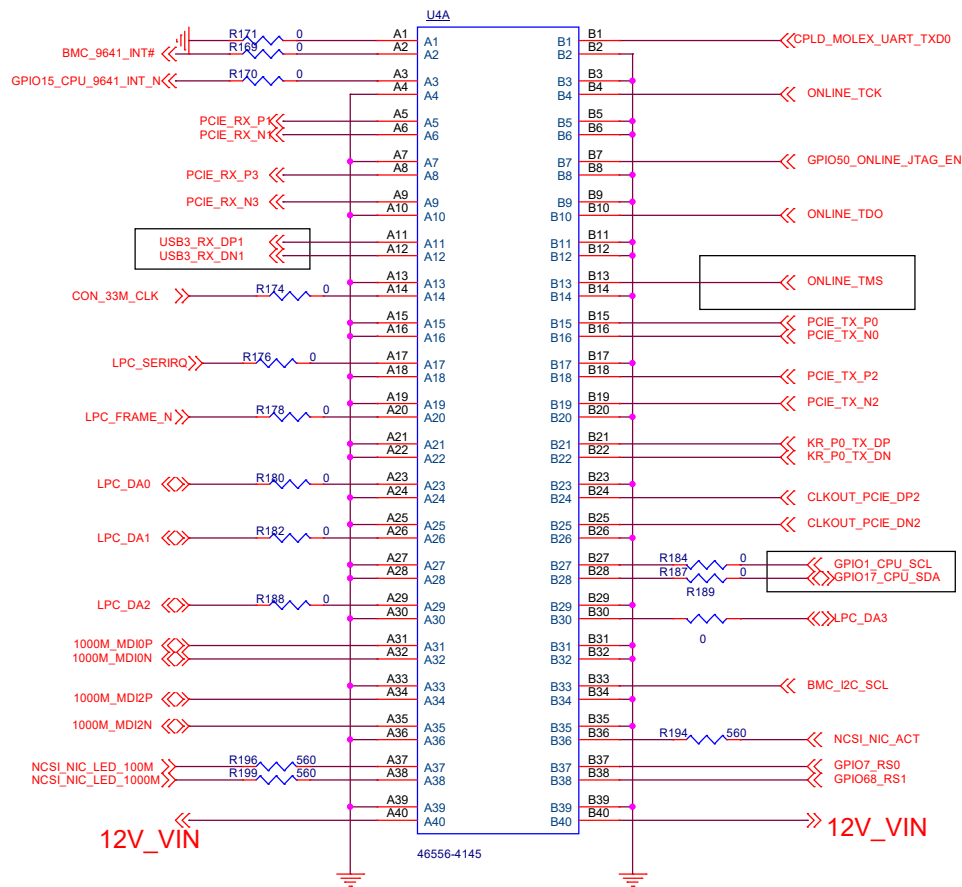




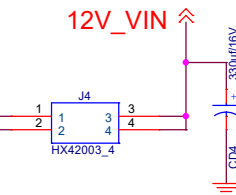
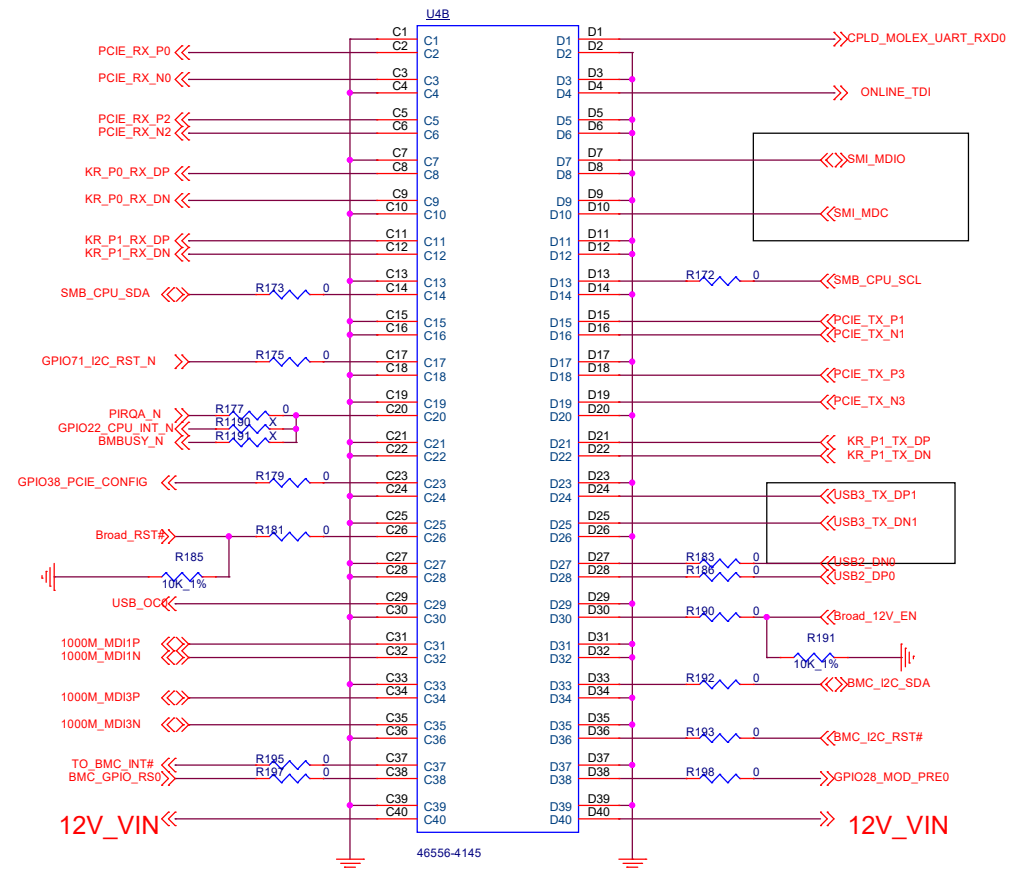
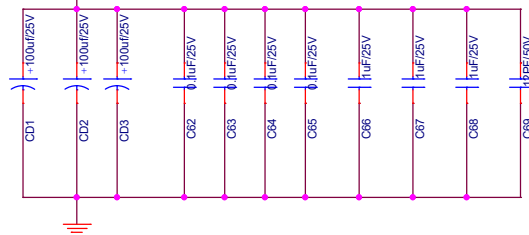


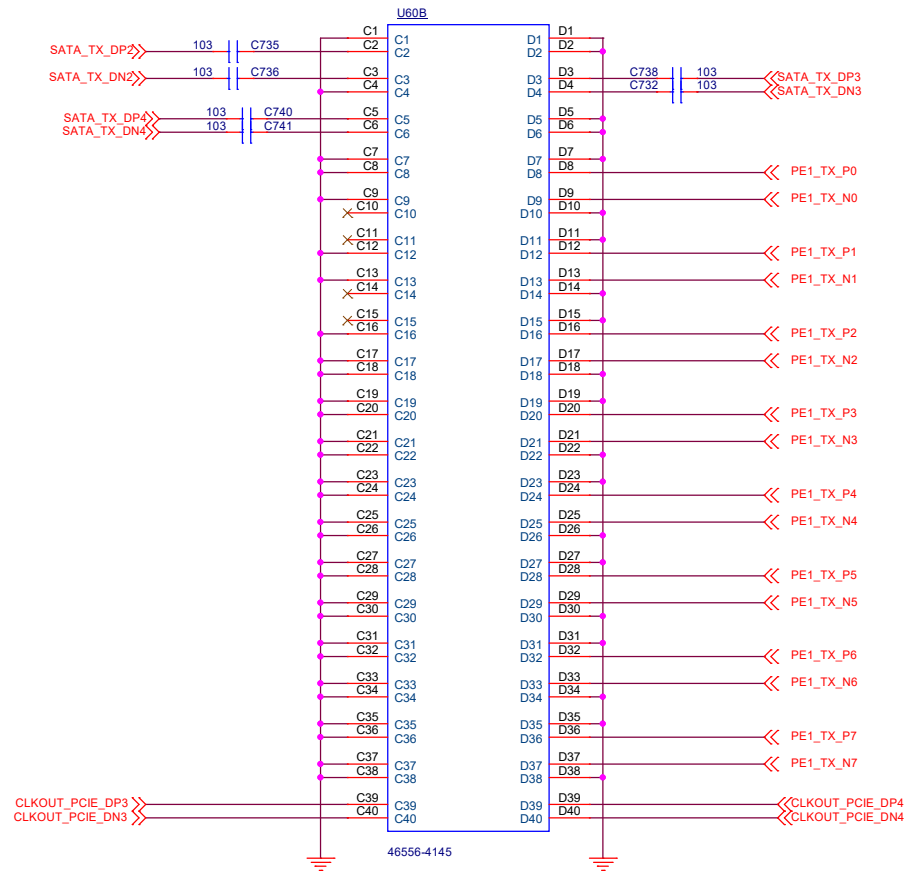
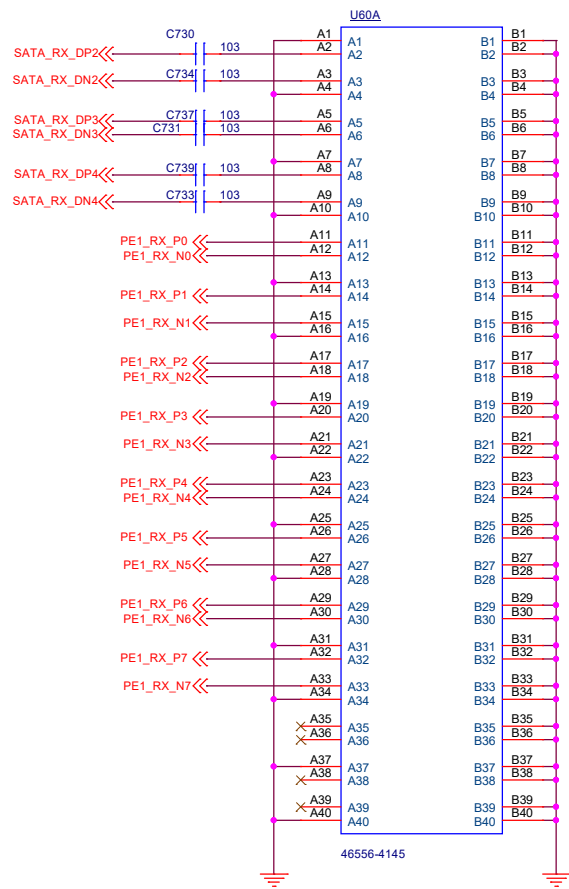


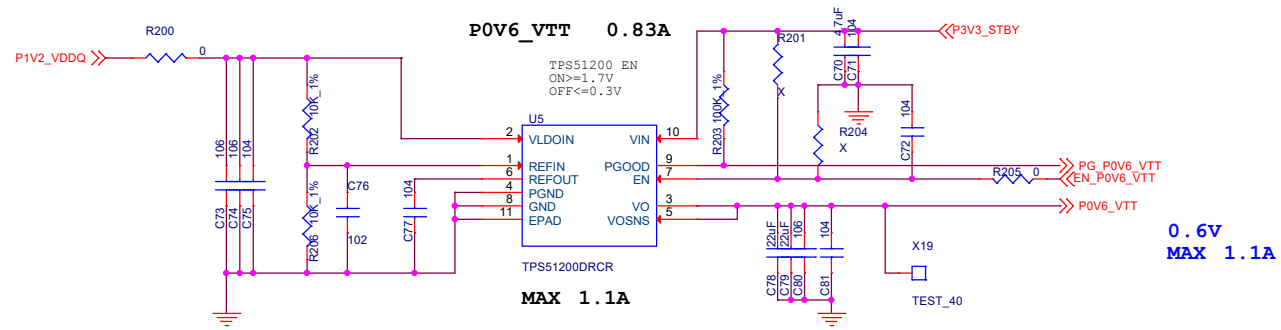


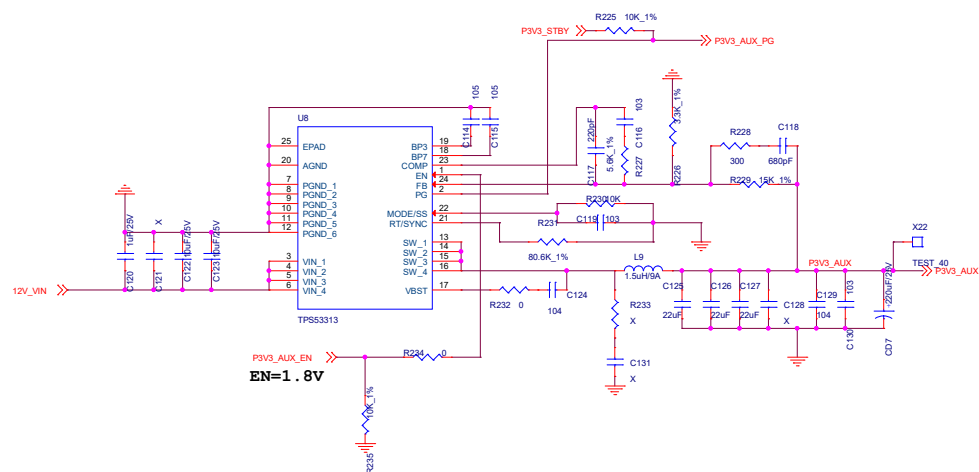
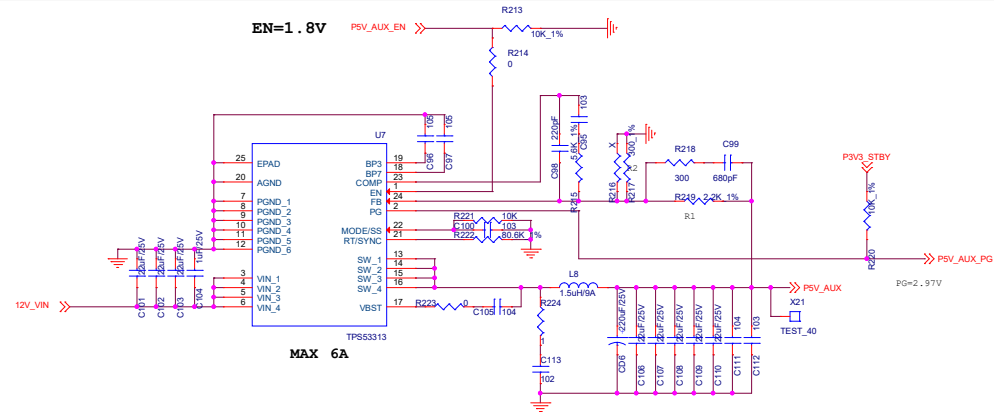
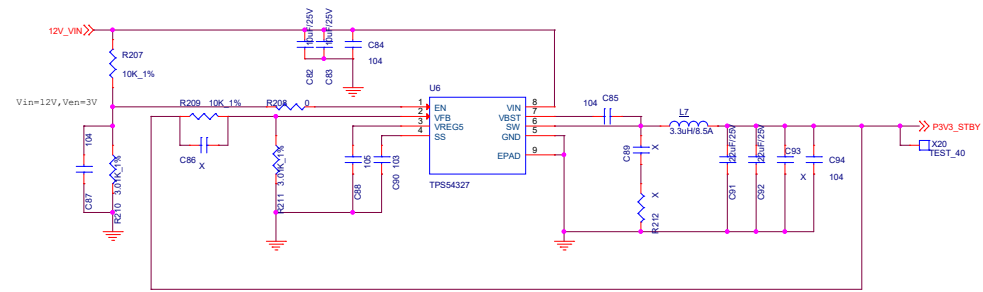


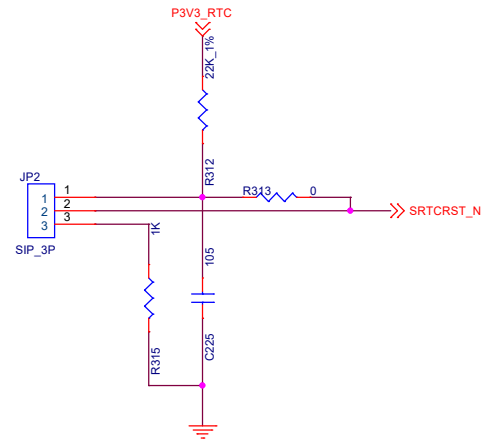
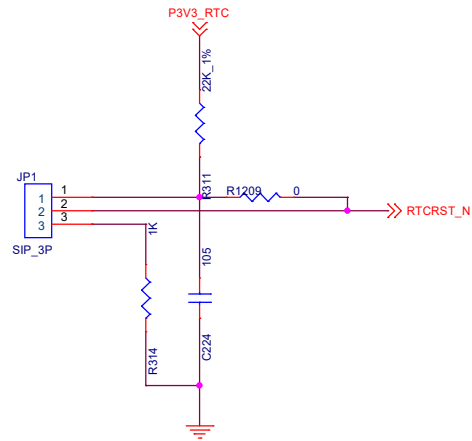
12V_VIN





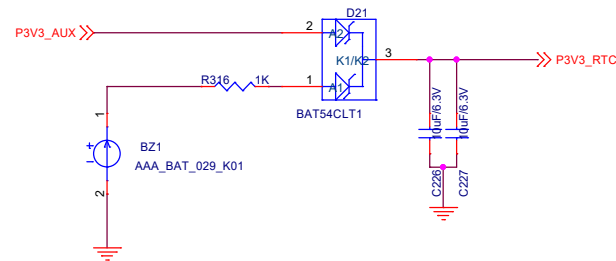


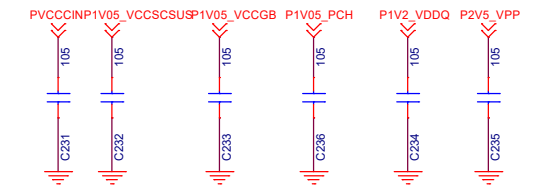
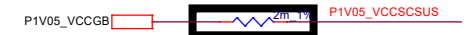
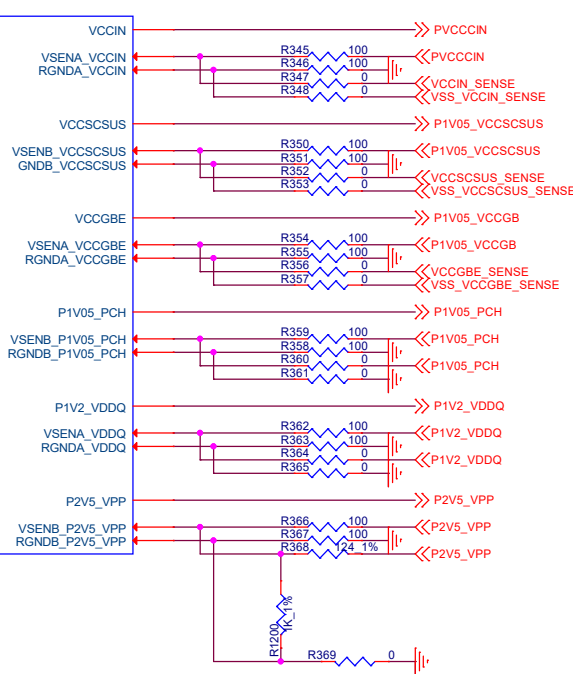
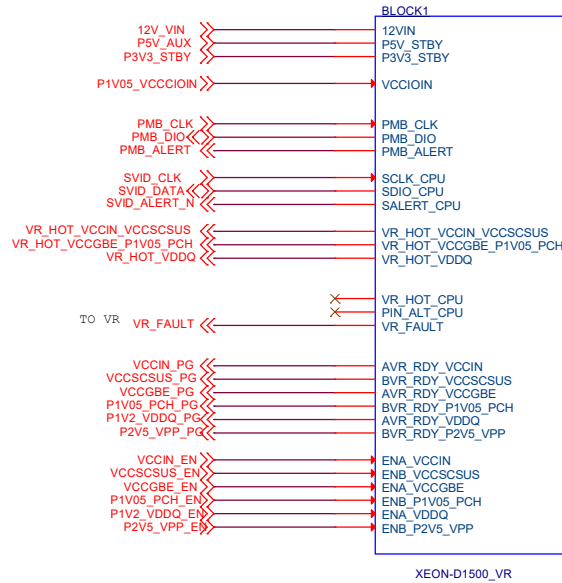
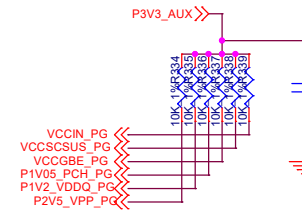
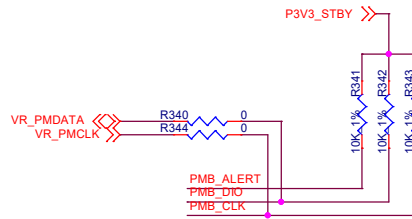
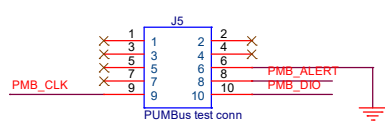




CMOS CLEAR JUMPER
 1-2:NOMARL RTC RESET(DEFAULT)
 2-3:CLEAR RTC REGISTERS
 THIS IS INSTALLED FOR ALL SYSTEMS IN PROTO

ME FIRMWARE UPDATE
 BIOS_RECOVERY_SWAP





PMBus And SVID Address

VR Name	SVID Address	I2C Address	Vboot
VCCIN	00h	C0h	1.7V
VCCSCSUS	01h	C0h	1.05V
P1V2_VDDQ	02h	D8h	1.2V
P2V5_VPP	NC	D8h	2.5V
VCCGBE	03h	C4h	1.05V
P1V05_PCH	05h	C4h	1.05V

$$R_{ADDRH} = R_{ADDRL} \left(\frac{V_{REF}}{V_{ADDR}} - 1 \right)$$

(2)

Table 5-2. SVID Slave Address Selection

RADDRL (kΩ)	Channel A SVID Address	Channel B SVID Address
154	0d	1d
115	2d	3d
86.6	0d	2d
64.9	1d	3d
49.9	0d	Not used (15d)
37.4	2d	Not used (15d)
28.0	4d	Not used (15d)
21.0	SVADDR_PRGM ⁽¹⁾ (PAGE 0)	SVADDR_PRGM ⁽¹⁾ (PAGE 1)

